

To: Facsimile Number: 571-273-8300

Total Pages Sent 9

From: Texas Instruments Incorporated  
Facsimile: 972-917-4418RECEIVED  
CENTRAL FAX CENTER

FEB 23 2006

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant: Bryan D. Sheffield, et al.

Docket Number: TI-35286

Serial No.: 10/663,144

Art Unit: 2185

Filed: 09/16/03

Examiner: Daniel Y. Kim

For: Method of Operating a Memory at High Speed Using a  
Cycle Ready Status Output Signal

## CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile  
to the U.S. Patent and Trademark Office on the date shown below:Karen Vertz  
Karen Vertz2-23-06  
Date

## FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT <u>111 (8 pages)</u>
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT
<input type="checkbox"/> DECLARATION	<input type="checkbox"/> NOTICE OF APPEAL
<input type="checkbox"/> ASSIGNMENT	<input type="checkbox"/> APPEAL <u>(# Pages)</u>
<input type="checkbox"/> FORMAL DRAWINGS	<input type="checkbox"/> ISSUE FEE & PUBLICATION FEE (1 Page)
<input type="checkbox"/> INFORMAL DRAWINGS	<input type="checkbox"/> REPLY BRIEF (IN TRIPLICATE) (# Pages)
<input type="checkbox"/> CONTINUATION APP'N	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Bryan D. Sheffield, et al.	
RECEIPT DATE & SERIAL NO.: Serial No.: <b>10/663,144</b>	
TITLE OF INVENTION: <b>Method of Operating a Memory at High Speed Using a Cycle Ready Status Output Signal</b>	
TI FILE NO.: <b>TI-35286</b>	DEPOSIT ACCT. NO.: <b>20-0668</b>
FAXED: 2-23-06 DUE: 2-23-06 ATTY/SECY: AKS/kjv	

This facsimile is intended only for the use of the address named and contains legally privileged and/or confidential information. If you are not the intended recipient of this telecopy, you are hereby notified that any dissemination, distribution, copying or use of this communication is strictly prohibited. Applicable privileges are not waived by virtue of the document having been transmitted by Facsimile. Any misdirected facsimiles should be returned to the sender by mail at the address indicated on this cover sheet.

Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265-5474

RECEIVED  
CENTRAL FAX CENTER  
FEB 23 2006

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Bryan D. Sheffield, et al. Art Unit: 2185  
Serial No.: 10/663,144 Examiner: Daniel Y. Kim  
Filed: 09/16/03 Docket: TI-35286  
For: METHOD OF OPERATING A MEMORY AT HIGH SPEED USING A CYCLE  
READY STATUS OUTPUT SIGNAL

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION  
I hereby certify that the above correspondence is  
being transmitted by facsimile to the U.S. Patent  
and Trademark Office at 571-273-8300 on the date  
shown below:

Karen Vertz  
Karen Vertz

2-23-06  
Date

AMENDMENT

In response to the Official Action in this case mailed  
November 23, 2005, please enter the following: